



#14 / D  
3/3/03  
Mallish

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. .... 09/810,595  
Filing Date ..... March 15, 2001  
Inventor ..... Belford T. Coursey  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2813  
Examiner ..... Yennhu B. Huynh  
Attorney's Docket No. .... MI22-1660  
Title: Memory Circuitry With Plurality of Capacitors Received Within an Insulative Layer  
Well (As Amended)

**RESPONSE TO DECEMBER 3, 2002 FINAL OFFICE ACTION**  
**TO ACCOMPANY RCE FILING**

To: Assistant Commissioner for Patents  
Box RCE  
Washington, D.C. 20231

From: Mark S. Matkin (Tel. 509-624-4276; Fax 509-838-3424)  
Wells St. John P.S.  
601 W. First Avenue, Suite 1300  
Spokane, WA 99201-3828

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FEB 27 2003  
TECHNOLOGY CENTER 2800

Responsive to the Office Action dated December 3, 2002, Applicant  
amends and remarks as follows:

**AMENDMENTS**

**In the Title**

✓ Please delete the existing Title and substitute the following in its place:

Memory Circuitry With Plurality of Capacitors  
Received Within an Insulative Layer Well

EV080427392